

Sheet 1 of 1

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| Form PTO-1449 (REV. 1/06) | | US Dept. of Commerce PATENT & TRADEMARK OFFICE | | ATTY DOCKET NO. 100629.03 | | APPLICATION NO. 10/625,617 | |
| INFORMATION DISCLOSURE STATEMENT (Use several sheets if necessary) | | | | APPLICANT(S) Tokuroh OZAWA et al. | | | |
| | | | | FILING DATE July 24, 2003 | | GROUP 2629 | |
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| U.S. PATENT DOCUMENTS | | | | | | | |
| Examiner Initials | Cite No. | Document Number | Date | Name | | | |
| SL | 1 | 5,463,279 | 10/31/1995 | KHORMAEI | | | |
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| SL | 2 | JP-A-08-214048 | 09/17/1996 | JAPAN | XX | XX | |
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| Examiner Initials | Cite No. | (Including Author, Title, Date, Pertinent Pages, etc.) | | | | | |
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| EXAMINER | | | | DATE CONSIDERED | | | |
| Jean Desperance | | | | 7-19-06 | | | |
| Examiner: Initial if citation considered, whether or not citation is in conformance with M.P.E.P. 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. | | | | | | | |

Date: July 12, 2006

Sheet 1 of 1

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| Form PTO-1449 (REV. 8-83) | | U.S. DEPT. OF COMMERCE PATENT & TRADEMARK OFFICE | | ATTY DOCKET NO. 100629.03 | | APPLICATION NO. 10/625,617 | |
| INFORMATION DISCLOSURE STATEMENT (Use several sheets if necessary) | | | | APPLICANTS Tokuroh OZAWA et al. | | | |
| | | | | FILING DATE July 24, 2003 | | GROUP 2674 | |
| U.S. PATENT DOCUMENTS | | | | | | | |
| EXAMINER INITIAL | | DOCUMENT NUMBER | DATE | NAME | CLASS | SUB CLASS | |
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| J.L. | 1 | JP 05-313195 (w/English-language translation) | 11/26/1993 | JAPAN | | | |
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| EXAMINER <i>John Key - ce</i> | | | | DATE CONSIDERED <i>6-9-05</i> | | | |
| Examiner: Initial if citation considered, whether or not citation is in conformance with M.P.E.P. 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. | | | | | | | |

Date: February 9, 2005

Form PTO-1449
(REV. 8-83)US Dept. of Commerce
PATENT & TRADEMARK OFFICEATTY DOCKET NO.
100629.03APPLICATION NO.
10/625,617

INFORMATION DISCLOSURE STATEMENT

(Use several sheets if necessary)

APPLICANT(S)
Tokuroh OZAWA et al.FILING DATE
July 24, 2003

GROUP

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| EXAMINER INITIAL | DOCUMENT NUMBER | DATE | NAME | CLASS | SUB CLASS |
|---------------------|-----------------|---------|----------|-------|--------------|
| J.L. | 5,463,279 | 10/1995 | KHORMAEI | 315 | 169.03 |
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| J.L. EP 0 849 721 A2 | 06/1998 | EUROPE | — | — |
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EXAMINER

Jean Dupont

DATE CONSIDERED

6-9-05

Examiner: Initial if citation considered, whether or not citation is in conformance with M.P.E.P. 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

Date: November 6, 2003



Sheet 1 of 1

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|---|----|---|---------|--------------------------------------|-------|-------------------------------|--|
| Form PTO-1449 (REV. 8-83) | | US Dept. of Commerce PATENT & TRADEMARK OFFICE | | ATTY DOCKET NO. 100629.03 | | APPLICATION NO. 10/625,617 | |
| INFORMATION DISCLOSURE STATEMENT (Use several sheets if necessary) | | | | APPLICANT(S) Tokuroh OZAWA et al. | | | |
| | | | | FILING DATE July 24, 2003 | | GROUP | |
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| J.L. | 4 | EP 0 633 741 A1 | 05/1995 | EUROPE | | | |
| J.L. | 5 | JP 08-241057 (w/ English abstract) | 09/1996 | JAPAN | | | |
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| J.L. | 10 | Lewis A. O. et al.: "Polysilicon TFT Circuit Design and Performance", IEEE Journal of Solid-State Circuits, IEEE Inc. New York, US, Vol. 27, No. 12, 1 December 1992 | | | | | |
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| EXAMINER <i>Chau Nguyen</i> | | | | DATE CONSIDERED 6-9-05 | | | |
| Examiner: Initial if citation considered, whether or not citation is in conformance with M.P.E.P. 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. | | | | | | | |

Date: October 24, 2003

17-24-03

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| Form PTO-1449 (REV. 8-83) | | US Dept. of Commerce PATENT & TRADEMARK OFFICE | | ATTY DOCKET NO. 100629.03 | | RULE 53(b) DIVISION OF APPLICATION NO. 10/267,834 | |
| INFORMATION DISCLOSURE STATEMENT (Use several sheets if necessary) | | | | APPLICANT(S) Takuroh OZAWA et al. | | | |
| FILING DATE July 24 2003 | | | | GROUP | | | |

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| J.L. | | EP-A1-0 653 741 | 05-95 | Europe | | |
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| J.L. | | Alan G. Lewis et al., Polysilicon TFT Circuit Design and Performance, IEEE Journal of Solid-State Circuits, December 1992, Vol. 27, No. 12, pgs. 1833-1842. | | | | |
| EXAMINER <i>Jean Legerance</i> | | | | DATE CONSIDERED 6-9-05 | | |
| Examiner: Initial if citation considered, whether or not citation is in conformance with M.P.E.P. 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. | | | | | | |

Date: July 24, 2003



Sheet 1 of 1

Form PTO-1449
(REV. 1/06)US Dept. of Commerce
PATENT & TRADEMARK OFFICEATTY DOCKET NO.
100629.03APPLICATION NO.
10/625,617

INFORMATION DISCLOSURE STATEMENT

(Use several sheets if necessary)

APPLICANT(S)
Tokuroh OZAWA et al.FILING DATE
July 24, 2003GROUP
2629

U.S. PATENT DOCUMENTS

| Examiner Initials | Cite No. | Document Number | Date | Name |
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DATE CONSIDERED

7/14/06

Examiner:

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Date: May 26, 2006

Sheet 1 of 1

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|---|-------------|--|-----------|--------------------------------------|-----------------------------|--------------------------------|--|
| Form PTO-1449 (REV. 1/06) | | US Dept. of Commerce PATENT & TRADEMARK OFFICE | | ATTY DOCKET NO. 100629.03 | | APPLICATION NO. 10/625,617 | |
| INFORMATION DISCLOSURE STATEMENT (Use several sheets if necessary) | | | | APPLICANT(S) Tokuroh OZAWA et al. | | | |
| | | | | FILING DATE July 24, 2003 | | GROUP 2629 | |
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| OTHER DOCUMENTS | | | | | | | |
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| | | | | 7/14/06 | | | |
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Date: May 9, 2006